



Sardar Vallabhbhai National Institute of Technology, Surat - 395007, Gujarat
Department of Electrical Engineering

Date: 15.12.2023

Notice(Revised)

With reference to the advertisement published at the Institute website for the post of Junior Research Fellow (JRF)-(01) on purely contract basis for a DRDO research project funded by *Naval Physical & Oceanographic Laboratory*, the offline interview of eligible candidates would be conducted in the **Department of Electrical Engineering at SVNIT, Room no: EN-17 (Seminar Hall) on 26/12/2023**. The project title is "USBL Tracking and Passive Localization of Targets using Correntropy and Moving Horizon Estimates"

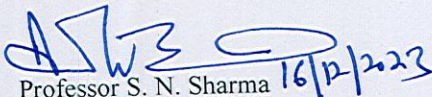
List of Eligible Candidates

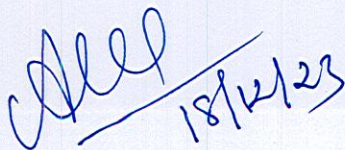
Sr. No	Name of the Candidate	Offline Interview at SVNIT Surat
1.	NANCY KUMARI	10:30 am onwards
2.	CHAITANIYA VIVEK DESHPANDE	
3.	RAKTANGI SWAIN	
4.	JAY ANILKUMAR BHAGAT	

Note: (1) Please bring all original certificates such as mark sheet, degree certificate, copy of GATE score card (if applicable) etc.

(2) All candidates are requested to report to the undersigned by 10:00 am on 26/12/2023.

(3) No TA/DA would be admissible.


Professor S. N. Sharma 16/12/2023
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Electrical Engineering
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18/12/23
Professor Ashish K Panchal

Chairman of DRCC and Head (Department of Electrical Engineering)